

HIGH SPEED HCMOS/TTL CLOCK OSCILLATORS

P5C-2

The P5C-2 is an all purpose plastic encapsulated oscillator, making it highly reliable and economical. This part drives up to 10 TTL loads, maintains low power consumption, and offers Tri-state Enable/Disable function.

FEATURES

- Standard Pin Configuration
- Tri-state Enable/Disable
- Economical Plastic Package
- Low Power Consumption
- HCMOS/TTL



Actual Size

OSCILLATORS

• ELECTRICAL CHARACTERISTICS (Ta = 25°C, VDD = 5.0V, CL = Max Load)

PARAMETERS	CONDITIONS	P5C-2		P5C-2T		P5C-2H		UNITS
		MIN	MAX	MIN	MAX	MIN	MAX	
Frequency Range (Fo)		1.025	26.000	26.000+	66.667	26.000+	66.667	MHz
Frequency Stability	All Conditions *	-100	+100	-100	+100	-100	+100	PPM
Temperature Range								
Operating (TOPR)		-10	+70	-10	+70	-10	+70	°C
Storage (TSTG)		-55	+125	-55	+100	-55	+100	
Supply Voltage (VDD)		+4.5	+5.5	+4.5	+5.5	+4.5	+5.5	V
Input Current (IDD)	No Load		25		35		35	mA
	Output Disabled (Iz)		12		28		20	
Output Symmetry	2.5V	40	60	---	---	40	60	%
	1.4V	45	55	45	55	---	---	
Rise Time (Tr)	1.0V ~ 4.0V		8		---		7	nS
	0.4V ~ 2.4V		8		5		---	
Fall Time (Tf)	4.0V ~ 1.0V		8		---		7	
	2.4V ~ 0.4V		8		5		---	
Output Voltage (VOL)	IOL = MAX		0.4		0.4		0.4	V
	IOH = MAX	4.6		2.4		4.6		
Output Current (IOL)	VOL = MAX		16		8		4.0	mA
	VOH = MIN		-0.4		-0.4		-4.0	
Output Load	TTL		10		5		---	TTL
	HCMOS		50		---		50	pF
Start-up Time (Ts)			4		10		10	mS
Output Enable/Disable Time			100		100		100	nS

* Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

*** An internal pullup resistor from pin 1 to pin 14 allows active output if pin 1 is left open.

See page 33 for environmental/mechanical specifications, test circuits, and output waveform.

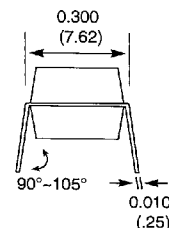
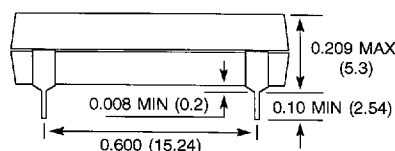
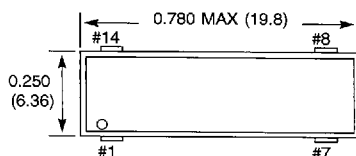
Note: ±50PPM stability also available.

Note: A 0.01µF bypass capacitor should be placed between VDD (Pin 14) and GND (Pin 7) to minimize power supply line noise.

All specifications subject to change without notice. Rev. 2/25/97

• ENABLE / DISABLE FUNCTION **

INH (Pin 1)	OUTPUT (Pin 8)
OPEN ***	ACTIVE
'1' Level VIH ≥ 2.0 V (P5C-2/P5C2H)	ACTIVE
'1' Level VIH ≥ 3.5 V (P5C-2T)	
'0' Level VIL ≤ 0.8 V (P5C-2/P5C2H)	High Z
'0' Level VIL ≤ 1.5 V (P5C-2T)	



Pin Connections

- #1 E/D ** #8 Output
- #7 GND #14 +5VDC

Inch dimensions shall govern.
All dimensions are in inches & parenthetically in millimeters.

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